

# 学术报告会

时间：2016年3月21日(周一)09:40-10:40

地点：电院群楼2-410会议室

## Digital and Memory Circuit Research at Waterloo

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### Abstract:

Integrated circuits are ubiquitous, and ever increasing low-power/energy requirements necessitate low-voltage operation of various circuit building blocks. However, as power supply voltage for circuits and embedded memories is reduced, it results into several design and reliability challenges. In this talk, we will discuss some of these challenges, and highlight recent research done at Waterloo.

### Biography:

**Manoj Sachdev** is a professor in electrical and computer engineering department at the University of Waterloo. At present he also serves as the department chair. His research interests include low-power and high-performance digital circuit design, mixed-signal circuit design, reliability and manufacturing issues of nano-metric integrated circuits. He has written five books, has contributed to over 200 technical articles in conferences and journals. He holds more than 35 granted and pending US patents in the broad area of VLSI circuit design and test. He, his students, and his colleagues have received several international awards. In 1997, at the IEEE European Design and Test Conference, he received the best paper award. In 1998, he was a co-recipient of the honorable mentioned award in the IEEE International Test Conference. He received the best panel award in 2004 IEEE VLSI Test Symposium. In 2011, he was a co-recipient of the best paper award in IEEE International Symposium on Quality Electronics Design. In 2015, he was co-recipient of the best poster award in IEEE Custom Integrated Circuits Conference. He is a Fellow of IEEE, and Engineering Institute of Canada.